

Electronic Acknowledgement Receipt

EFS ID:	10818267
Application Number:	10801503
International Application Number:	
Confirmation Number:	1822
Title of Invention:	FRACTIONAL-TYPE PHASE-LOCKED LOOP CIRCUIT WITH COMPENSATION OF PHASE ERRORS
First Named Inventor/Applicant Name:	Guido Gabriele Albasini
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Filer Authorized By:	Kevin Duane Jablonski
Attorney Docket Number:	2110-111-03
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Certificate of Correction	2110-111-03_REQCOC_20110825_AsFiled.pdf	68665 dc7504cd9d722db095076e32b18814541c236	no	2

Warnings:

Information:

2	Fee Worksheet (SB06)	fee-info.pdf	30300 300b9c23dd412716b1308b715a5d7d55d6d89a	no	2
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Warnings:

Information:

Total Files Size (in bytes):	98965
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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